

## Refine Search

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### Search Results -

Terms	Documents
semiconductor and (trench or groove) and pad and mask and etch	4

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**Database:**

US Pre-Grant Publication Full-Text Database  
 US Patents Full-Text Database  
 US OCR Full-Text Database  
 EPO Abstracts Database  
**JPO Abstracts Database**  
 Derwent World Patents Index  
 IBM Technical Disclosure Bulletins

**Search:**

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Recall Text
Clear
Interrupt

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### Search History

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**DATE:** Wednesday, July 14, 2004    [Printable Copy](#)    [Create Case](#)

<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u>
side by side			result set
<i>DB=JPAB; PLUR=YES; OP=ADJ</i>			
<u>L6</u>	semiconductor and (trench or groove) and pad and mask and etch	4	<u>L6</u>
<i>DB=EPAB; PLUR=YES; OP=ADJ</i>			
<u>L5</u>	semiconductor and (trench or groove) and pad and mask and etch	2	<u>L5</u>
<i>DB=USPT; PLUR=YES; OP=ADJ</i>			
<u>L4</u>	semiconductor and (trench or groove) and pad and mask and etch	4420	<u>L4</u>
<i>DB=PGPB; PLUR=YES; OP=ADJ</i>			
<u>L3</u>	semiconductor and (trench or groove) and pad and mask and etch	1907	<u>L3</u>
<i>DB=TDBD; PLUR=YES; OP=ADJ</i>			
<u>L2</u>	semiconductor and (trench or groove) and pad and mask and etch	6	<u>L2</u>
<i>DB=DWPI; PLUR=YES; OP=ADJ</i>			
<u>L1</u>	semiconductor and (trench or groove) and pad and mask and etch	204	<u>L1</u>

END OF SEARCH HISTORY